



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Yoshinori UEDA

Serial No.: 09/431,593

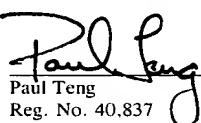
Group Art Unit: 2815

Date Filed: November 1, 1999

Examiner: Matthew E. Warren

For: SEMICONDUCTOR DEVICE HAVING AN INTEGRAL RESISTANCE ELEMENT

I hereby certify that this correspondence is being deposited this date with the U.S. Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


 Paul Teng
 Reg. No. 40,837

March 24, 2004
 Date

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Mail Stop RCE
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT

A Notice of Allowance was issued December 29, 2003 by the U.S. Patent Office in connection with the subject application. This Information Disclosure Statement is being submitted concurrently with a Request for Continued Examination under 37 C.F.R. §1.114, pursuant to the applicant's duty of disclosure, in order to bring the information listed in the attached Form PTO-1449 to the attention of the Examiner. Copies of the information identified herein are also provided.

It is respectfully requested that the information cited in the attached Form PTO-1449 be considered by the Examiner in connection with the above-identified patent application, and that such art be made of record in said application.

The citation of the listed items is not a representation that they constitute a complete or exhaustive listing of the relevant art or that these items are prior art. The items listed are

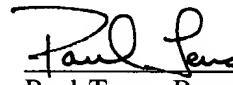
submitted in good faith, but are not intended to substitute for the Examiner's search. It is hoped, however, that in addition to apprising the Examiner of the particular items, they will assist in identifying fields of search and in making as full and complete a search as possible.

The filing of this Information Disclosure Statement is not an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

The Office is hereby authorized to charge any additional fees which may be required for consideration of this Information Disclosure Statement and to credit any overpayment to our Deposit Account No. 03-3125.

Early and favorable consideration of the case is respectfully requested.

Respectfully submitted,



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Form PTO-1249

U.S. Department of Commerce
Patent and Trademark OfficeAtty. Docket No.
2271/60617Serial No.
09/431,593

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Applicant:
Yoshinori UEDAFiling Date:
November 1, 1999Group Art Unit:
2815

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number							Date	Name	Class	Subclass	Filing Date if Appropriate

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
JP 5 9- 1 9 1 3 6 8	10-30-84	Japan				Abstract	
JP 6 1- 1 9 1 0 6 0	8-25-86	Japan				Abstract	
JP 6 1- 2 0 8 8 6 2	9-17-86	Japan				Abstract	
JP 4 - 2 2 1 6 4	1-27-92	Japan				Abstract	
JP 9 - 3 6 3 1 0	2-7-97	Japan				Abstract	
JP 9 - 9 7 8 7 6	4-8-97	Japan				Abstract	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609: Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.